Addendum to Series 742 Apollo II User's Guide
5/15/96

Page 1   There is a typo in the fourth bulleted item from the top of the page. "ZIP" should be "ZIF".

Page 4   Replace the drawing of the motherboard with the following:

Page 6 CPU Speed Change the table on this page as follows:

Change the contents of the table row for **50 MHz External Speed and 75 MHz Internal Speed** to *JC1 OPEN* and *JC2 OPEN.*

The J6 setting remains the same.

Page 9   The first graphic on this page does not point to pin 1 of the CPU socket. Replace this drawing with the following:

Page 10   **System Memory**

Add "1 MB x 36 SIMMS" to the list of supported SIMM types.

Page 13   **Configure Cache Memory**

The Apollo II motherboard is shipped with a cache module. The SRAM and Tag RAM sockets on the motherboard are empty when shipped. You do not have to change JPW1, JPW2, JPW3, and JPW4. The factory settings are correct for the cache module.

Page 14   **DIP Cache**

Delete this heading and the sentence to the right of this heading.

Page 14   Replace the **Select SRAM Type** graphic with the following:

Page 22   Connect Mouse Cable

Delete all text after this heading. The mouse connector pinout is correct.

Page 24   Speaker Connector

Delete the last line of text. The Apollo II motherboard does not have a built-in speaker.

Page 27   Parallel Port

There is a typo in the second line. It is a *26*-pin-to-DB25 cable.

Page 30   Change text in IDE Connector Drawing

Replace "J1 Secondary IDE Connector" with "IDE1 Secondary IDE Connector" in the middle of the drawing.

Replace "J8 Primary IDE Connector " with "IDE2 Primary IDE Connector".